Tutorial: Extending Loop Transformation Frameworks to Irregular Applications (aka Math for Irregular Codes)

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Transformation Frameworks

Motivation

- Intermediate representations for computations
- Transformation specifications and code generation after transformation
- Composition of transformations
- Data dependence representation and thus legality checks for composed transformations

Covering in Today's Tutorial

- Polyhedral Model for representing computations with affine loop bounds and array accesses
- Polyhedral compilation for sparse-immutable computations
- Sparse Polyhedral Framework (SPF) for sparse matrix/tensor computations with indirect array accesses
- PolyRec Framework for recursive irregular computations

Polyhedral model: Loop transformations improve performance!

Example (dgemm)

```
/* C := alpha*A*B + beta*C */
for (i = 0; i < ni; i++)
for (j = 0; j < nj; j++)
S1: C[i][j] *= beta;
for (i = 0; i < ni; i++)
for (j = 0; j < nj; j++)
for (k = 0; k < nk; ++k)
S2: C[i][j] += alpha * A[i][k] * B[k][j];</pre>
```

Loop transformation: permute(i,k,S2)

Execution time (in s) o	on this laptop,	GCC 4.2, ni=nj=nk=5 ⁻	12
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version	-00	-01	-02	-03 -vec
original	1.81	0.78	0.78	0.78
permute	1.52	0.35	0.35	0.20

http://gcc.gnu.org/onlinedocs/gcc-4.2.1/gcc/Optimize-Options.html

Another Example: FDTD

Example (fdtd-2d)

Loop transformation: polyhedralOpt(fdtd-2d)

Execution time (in s) on this laptop, GCC 4.2, 64x1024x1024

version	-00	-01	-02	-O3 -vec
original	2.59	1.62	1.54	1.54
polyhedralOpt	2.05	0.41	0.41	0.41

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Doing such transformations by hand is NOT FEASIBLE!

Example (fdtd-2d tiled)

for (c0 = 0; c0 <= (((ny + 2 * tmax + -3) * $32 < 0?((32 < 0?-((-(ny + 2 * tmax + -3) + 32 + 1) / 32)) : -((-(ny + 2 * tmax + -3) + 32 - 1) / 32))) : (ny + 2 * tmax + -3) / 32)); ++c0) {$

#pragma omp parallel for private(c3, c4, c2, c5)

for (c1 = (((c0 * 2 < 0?-(-c0 / 2) : ((2 < 0?(-c0 + -2 - 1) / -2 : (c0 + 2 - 1) / 2))) > (((32 * c0 + -tmax + 1) * 32 < 0?-(-(32 * c0 + -tmax + 1) / 32) : ((32 < 0?(-(32 * c0 + -tmax + 1) + -32 - 1) / -32 : (32 * c0 + -tmax + 1 + 32 - 1) / 32)))?((c0 * 2 < 0?-(-c0 / 2) : ((2 < 0?(-c0 + -2 - 1) / -2 : (c0 + 2 - 1) / 2)))) : (((32 * c0 + -tmax + 1) * 32 < 0?-(-(32 * c0 + -tmax + 1) / 32) : ((32 < 0?(-(32 * c0 + -tmax + 1) + -32 - 1) / -32 : (32 * c0 + -tmax + 1 + 32 - 1) / 32)))); c1 <= ((((((((ny + tmax + -2) * 32 < 0?((32 < c0 + -tmax + 1) + -32 - 1) / -32 : (32 * c0 + -tmax + 1 + 32 - 1) / 32))); c1 <= ((((((((ny + tmax + -2) * 32 < 0?((32 < 0?-((-(ny + tmax + -2) + 32 + 1) / 32)) : -((-(ny + tmax + -2) + 32 - 1) / 32))); c1 <= (((((((ny + tmax + -2) / 32)) < (((32 * c0 + ny + 30) * 64 < 0?((64 < 0?-((-(32 * c0 + ny + 30) + 64 + 1) / 64)))) : (32 * c0 + ny + 30) / 64))?(((ny + tmax + -2) * 32 < 0?((32 < 0?-((-(ny + tmax + -2) / 32))) : (ny + tmax + -2) / 32)) : ((((32 * c0 + ny + 30) / 64))?(((ny + tmax + -2) / 32))) : ((((32 * c0 + ny + 30) + 64 - 1) / 64)))) : (32 * c0 + ny + 30) / 64))?(((ny + tmax + -2) / 32)) : ((((32 * c0 + ny + 30) + 64 - 1) / 64)))) : (32 * c0 + ny + 30) + 64 - 1) / 64))) : (32 * c0 + ny + 30) + 64 - 1) / 64))) : (32 * c0 + ny + 30) + 64 < 0?((64 < 0?-((-(32 * c0 + ny + 30) + 64 + 1) / 64)) : -((-(ny + tmax + -2) + 32 - 1) / 32))) : (ny + tmax + -2) / 32)) : (((-ny + tmax + -2) + 32 - 1) / 32))) : (ny + tmax + -2) + 32 < 0?((32 < c0 - (-(ny + tmax + -2) + 32 - 1) / 32))) : (ny + tmax + -2) + 32 < 0?((32 < 0?-((-(ny + tmax + -2) + 32 + 1) / 32)) : -((-(ny + tmax + -2) + 32 - 1) / 32))) : (ny + tmax + -2) + 32 < 0?((32 < 0?-((-(ny + tmax + -2) + 32 + 1) / 32)) : -((-(ny + tmax + -2) + 32 - 1) / 32))) : (ny + tmax + -2) + 32 < 0?((32 < 0?-((-(ny + tmax + -2) + 32 + 1) / 32))) : (ny + tmax + -2) + 32 < 0?((32 < 0?-((-(ny + tmax + -2) + 32 + 1) / 32))) : (ny + tmax + -2) + 32 < 0?((32 < 0?-((-(ny + tmax + -2) + 32 + 1) / 32))) : (ny + tmax + -2) + 32 < 0?((32 < 0?-((-(ny + tmax + -2) + 32

for $(c^2 = c^0 + -c^1; c^2 <= (((((tmax + nx + -2) * 32 < 0?((32 < 0?-((-(tmax + nx + -2) + 32 + 1) / 32)) : -((-(tmax + nx + -2) + 32 - 1) / 32))) : (tmax + nx + -2) / 32)) < (((32 * c^0 + -32 * c^1 + nx + 30) * 32 < 0?((32 < 0?-((-(32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32)))) : (32 * c^0 + -32 * c^1 + nx + 30) / 32))? (((tmax + nx + -2) * 32 < 0?((32 < 0?-((-(tmax + nx + -2) + 32 + 1) / 32)) : -((-(tmax + nx + -2) + 32 + 1) / 32))) : (tmax + nx + -2) + 32 < 0?((32 < 0?-((-(tmax + nx + -2) + 32 - 1) / 32)))) : (tmax + nx + -2) / 32)) : (((32 * c^0 + -32 * c^1 + nx + 30) * 32 < 0?((32 < 0?-((-(32 * c^0 + -32 * c^1 + nx + 30) * 32 < 0?((32 < 0?-((-(32 * c^0 + -32 * c^1 + nx + 30) * 32 < 0?((32 < 0?-((-(32 * c^0 + -32 * c^1 + nx + 30) * 32 < 0?((32 < 0?-((-(32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32)))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) + 32 - 1) / 32))) : (32 * c^0 + -32 * c^1 + nx + 30) / 32))))) : (32 * c^0 + -32 * c^1 + nx + 30) / 32))))) : (32 * c^0 + -32 * c^1 + nx + 30) / 32))))) : (32 * c^0 + -32 * c^1 + nx + 30) / 32)))) : (32 * c^0 + -32 * c^1 + nx + 30)$

Performance gain: $2-6 \times$ on modern multicore platforms

RoadMap for Tutorial: Math for Irregular Codes

Concepts

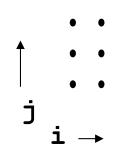
- Polyhedral model review
- Sparse computations as union of dense computations
- Sparse Polyhedral Framework (SPF)
- Polyrec

Hands On Tutorial Goals

- Specify affine transformations in ISCC
- Handle irregular loop bounds in ISCC
- Demo of data dependence analysis for SPF
- Demo polyrec

Representing Loops with Math (Matrices)

Original code

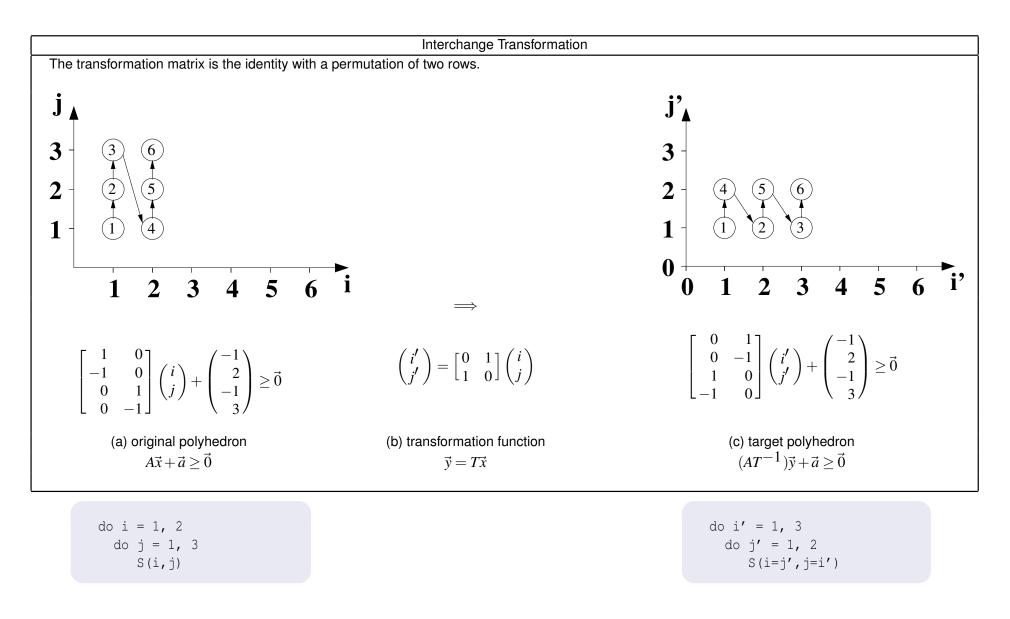


Represent the iteration space

- -As an intersection of inequalities
- -The iteration space is the integer tuples within the intersection

Bounds:

Affine Transformations



Goal: Learn How to Use ISCC

ISCC

- Calculator for ISL (Integer Set Library)
- <u>http://compsys-tools.ens-lyon.fr/iscc/</u>
- Author: Sven Verdoolaege
- See Barvinok documentation online for a user manual

ISCC Online Demonstrator	Barvinok documentation	SL documentation	L Barvinok sources	
ISCC Online Demonstrator ISCC script I := [N] -> {S[i,j] : 0<=i <n -="" :="{S[i,j]" codegen="" i;="" t=""> S[j,i]}; codegen (T*I);</n>	Reset Execut	e Output ISCC output for (int c0 = for (int c1 S(c0, c1) for (int c1 S(c1, c0) Summar	1; c0 < N; c0 += 1; = 0; c1 < c0; c1 += ; 0; c0 < N - 1; c0 - = c0 + 1; c1 < N; c; y mmand iscc	= 1) ⊨= 1)
Output format isl) isi-0.16.1 polylib-5.22.5 ntl-

Create a macro for statement in C

```
#define S(i,j) A[i][j] = exp((i)+(j))
for (i=0; i<N; i++)
  for (j=0; j<i; j++)
     S(i,j);</pre>
```

Iterations in loops described as a Set in ISCC

I := $[N] \rightarrow \{S[i,j] : 0 \le i \le N \text{ and } 0 \le j \le i\};$

ISCC: Loop Interchange Transformation

Generate the loop bounds for the Set I

```
// Input
I := [N] -> {S[i,j] : 0<=i<N and 0<=j<i};
codegen I;
// Output
for (int c0 = 1; c0 < N; c0 += 1)
  for (int c1 = 0; c1 < c0; c1 += 1)
    S(c0, c1);</pre>
```

Generate after applying *Loop Interchange*

```
// Input: Transformation function
T := {S[i,j] -> [j,i]};
codegen (T*I);
// Output
for (int c0 = 0; c0 < N - 1; c0 += 1)
   for (int c1 = c0 + 1; c1 < N; c1 += 1)
        S(c1, c0);</pre>
```

ISCC Determines Old Iterators as Function of New Iterators

Original Loop Nests and Transformation

```
codegen (T_fusion*Domain);
```

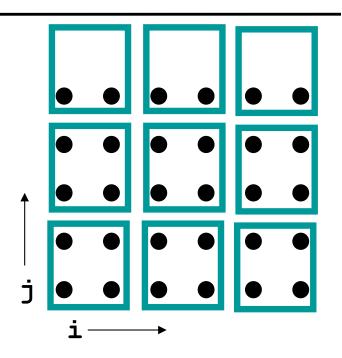
Resulting code with old iterators as function of new iterators

```
// Output
for (int c1 = 1; c1 < N; c1 += 1) {
    S1(0, c1, 0);
    S2(1, c1 - 1, 0);
}</pre>
```

Tiling

A loop transformation that ...

- groups iteration points into tiles that are executed atomically
- can improve spatial and temporal data locality
- can expose larger granularities of parallelism



Specifying Tiling

Rectangular tiling

- tile size vector
$$(s_1, s_2, ..., s_d)$$

- tile offset,
$$(o_1, o_2, ..., o_d)$$

Possible Transformation Mappings

- creating a tile space $\{[i, j] \rightarrow [ti, tj, i, j] \mid ti = floor((i - o_1)/s_1)$ $\land tj = floor((j - o_2)/s_2)\}$

- keeping tile iterators in original iteration space $\{[i, j] \rightarrow [ii, jj, i, j] \mid ii = s_1 floor((i - o_1)/s_1) + o_1$ $\land jj = s_2 floor((j - o_2)/s_2) + o_2\}$

i

Introduction to the Polyhedral Model

Using ISCC to do code generation for tiling

Iteration space: S := { s[i,j] : 1<=i<=6 && 1<=j<=5 };
Tiling specification
T :={s[i,j]->[ti,tj,i,j]: ti=(i-1)/2&&tj=(j-1)/2};

codegen (T*S); // doesn't work in iscc

Getting rid of integer divison

Polyhedral Model

Some History

- [Banerjee90] Uptal Banerjee, "Unimodular transformations of double loops," In Advances in Languages and Compilers for Parallel Computing, 1990.
- [Wolf & Lam 91] Wolf and Lam, "A Data Locality Optimizing Algorithm," In Programming Languages Design and Implementation, 1991.
- [Kelly and Pugh 95] Kelly and Pugh, "A unifying framework for iteration reordering transformations," In IEEE First International Conference on Algorithms and Architectures for Parallel Processing (ICAPP)
- [Feautrier 96] Paul Feautrier, "Automatic Parallelization in the Polytope Model," In The Data Parallel Programming Model.

Polyhedral Model

Some key components

- Representing loops as sets
- Representing data dependences as dependence vectors
- Representing transformations as functions
- Applying transformations to generate transformed code